

## T-RAM readies embedded SRAM, gains \$40 million in funding

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SAN JOSE, Calif. — Memory startup T-RAM Semiconductor Inc. on Wednesday (April 20) said that it is readying its first product — an embedded memory line.

T-RAM also announced the completion of its Series C financing, which amounted to \$40 million. This brings the total funds raised by the company to \$86 million.

The round was led by InterWest Partners and further supported by CenterPoint Ventures, Mayfield, US Venture Partners, Tallwood Venture Capital and New Enterprise Associates.

T-RAM also reports that Victor Westerlind, a principal at InterWest Partners, will join the board of directors.

T-RAM (San Jose) is a five-year-old company that has developed a memory cell expected to surpass today's 6T-SRAM (six transistor-SRAM) devices, claimed Kenneth Young, CEO of the company.

"With working silicon verifying the success of our first high-performance memory chips, we are now ready to commercialize this technology," he said in a statement.

"In fact, our first memory chips will soon be launched -- with densities ranging from 9-Mbit up to 72-Mbit -- the highest commercially available density from any SRAM manufacturer today," he said. "Initial applications for this first-generation technology will be focused on the high-performance, high-density synchronous SRAM (SSRAM) market, which is dominated by high-end wired products in both communications and computing."

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